

Preliminary Amendment
National Stage Entry of PCT/JP2003/12497
Q94138

Please delete the present Abstract of the Disclosure.

Please add the following new Abstract of the Disclosure:

A CPU calculates, based on a frequency command value for driving a motor and on a state quantity of the motor, an output-voltage command value in which only the phase advances while amplitude is constant in each calculation period, without reducing the calculation period. An ASIC reflects the output-voltage command value in a triangular wave signal in a time-series order to compare with each other, and outputs a PWM signal to a switching circuit. Thus, a waveform of an output voltage is made close to a sine wave irrespective of an output frequency being high or low, and a processing load of the CPU is reduced.